

Patent Application No. 10/000,000

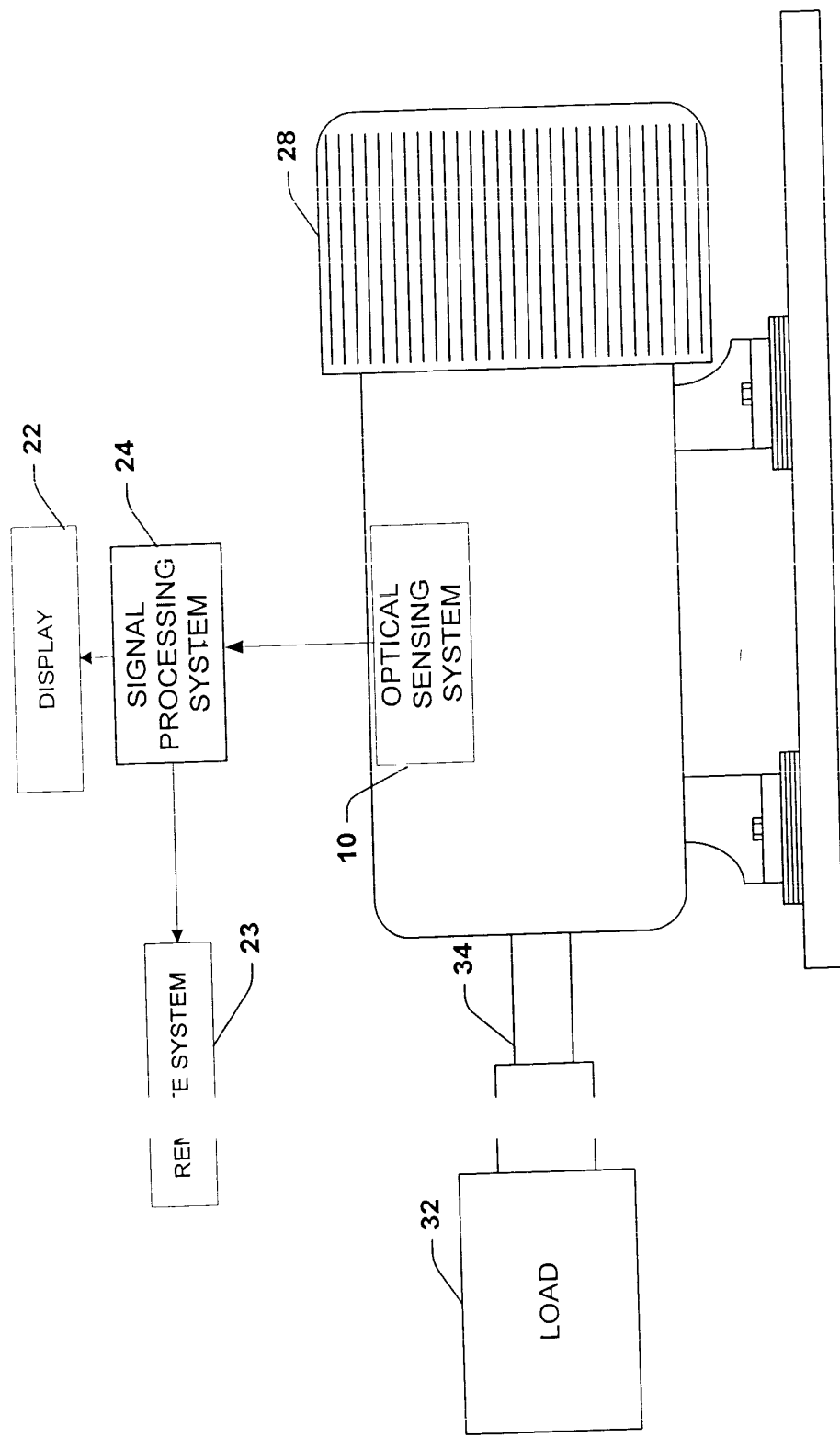


Fig. 1

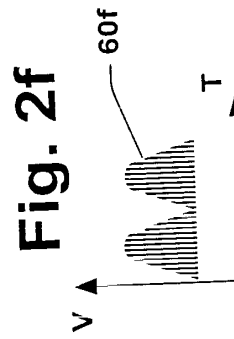
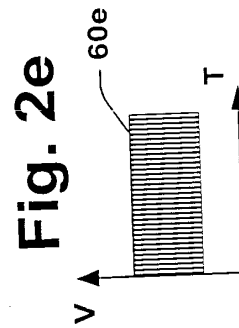
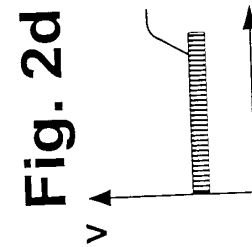
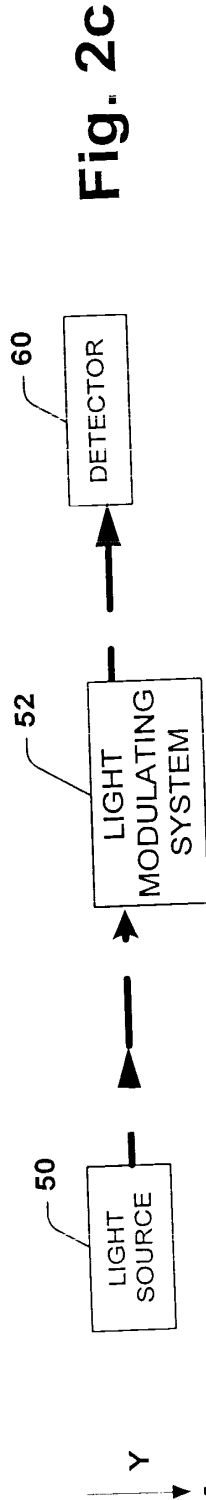
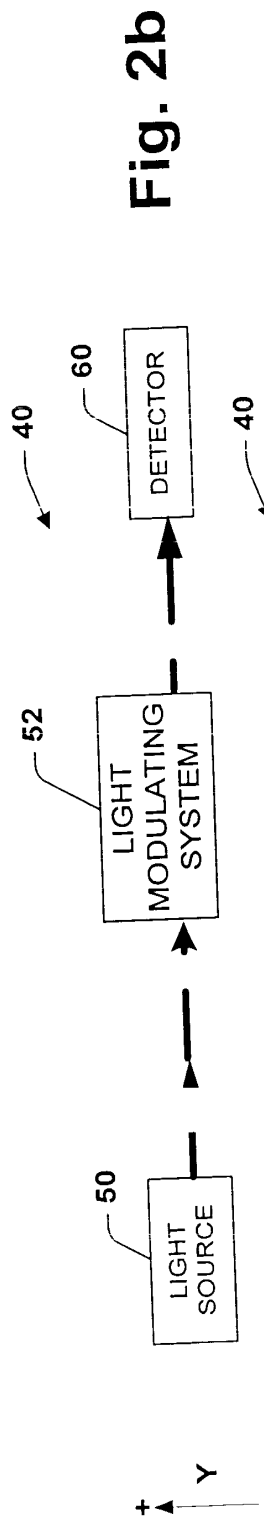
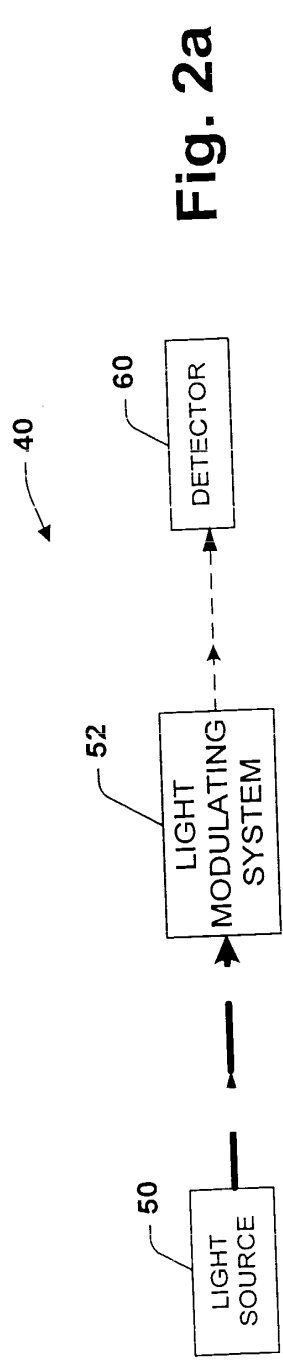


Fig. 2g

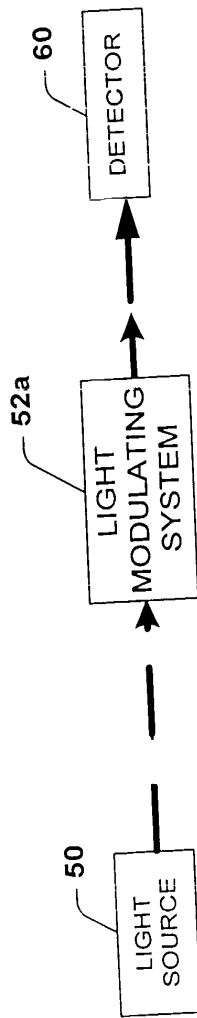


Fig. 2h

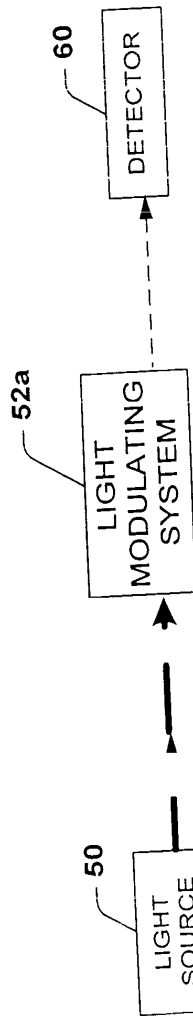


Fig. 2i

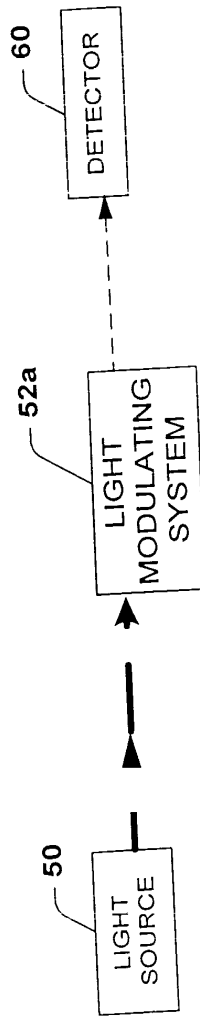


Fig. 2j

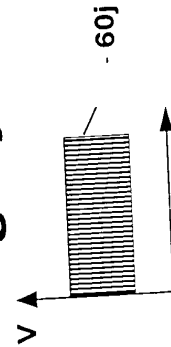


Fig. 2k

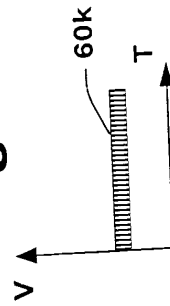


Fig. 2l



US 6,100,000 A

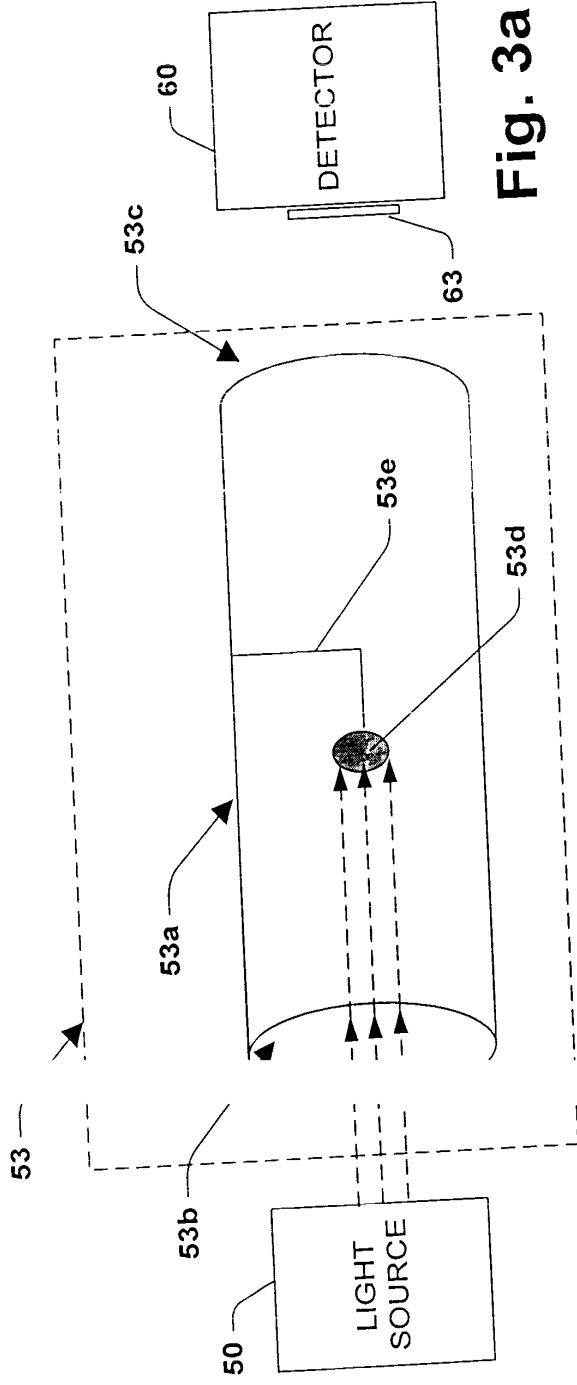


Fig. 3a

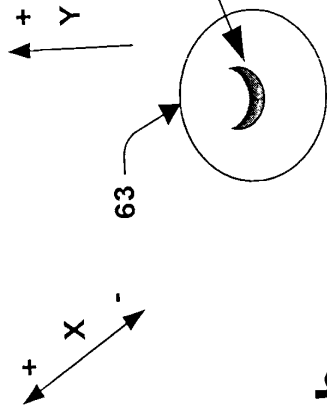


Fig. 3b

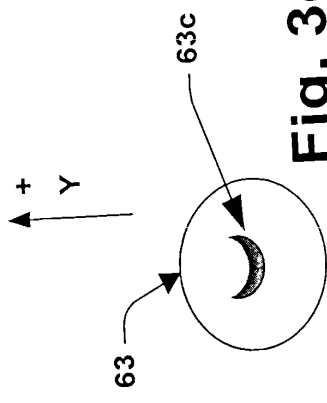


Fig. 3c

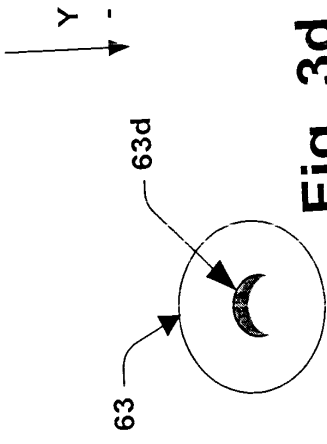


Fig. 3d

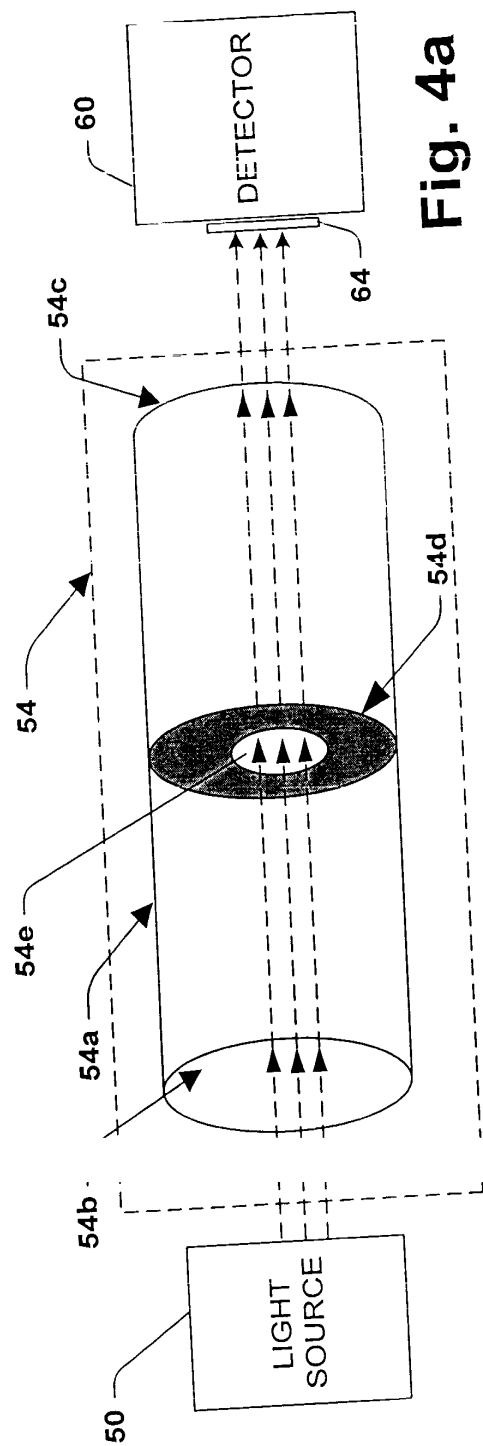


Fig. 4a

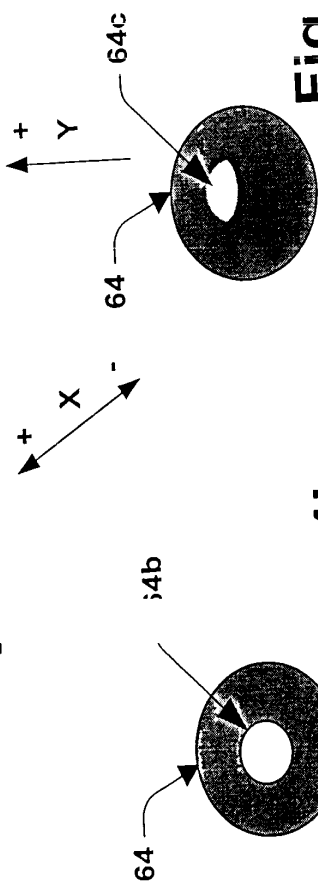


Fig. 4b

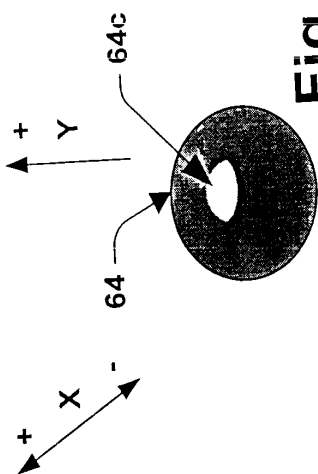


Fig. 4c

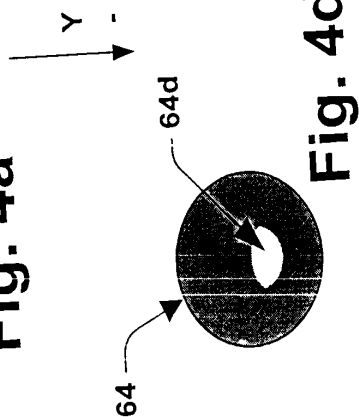


Fig. 4d

FIG. 4e

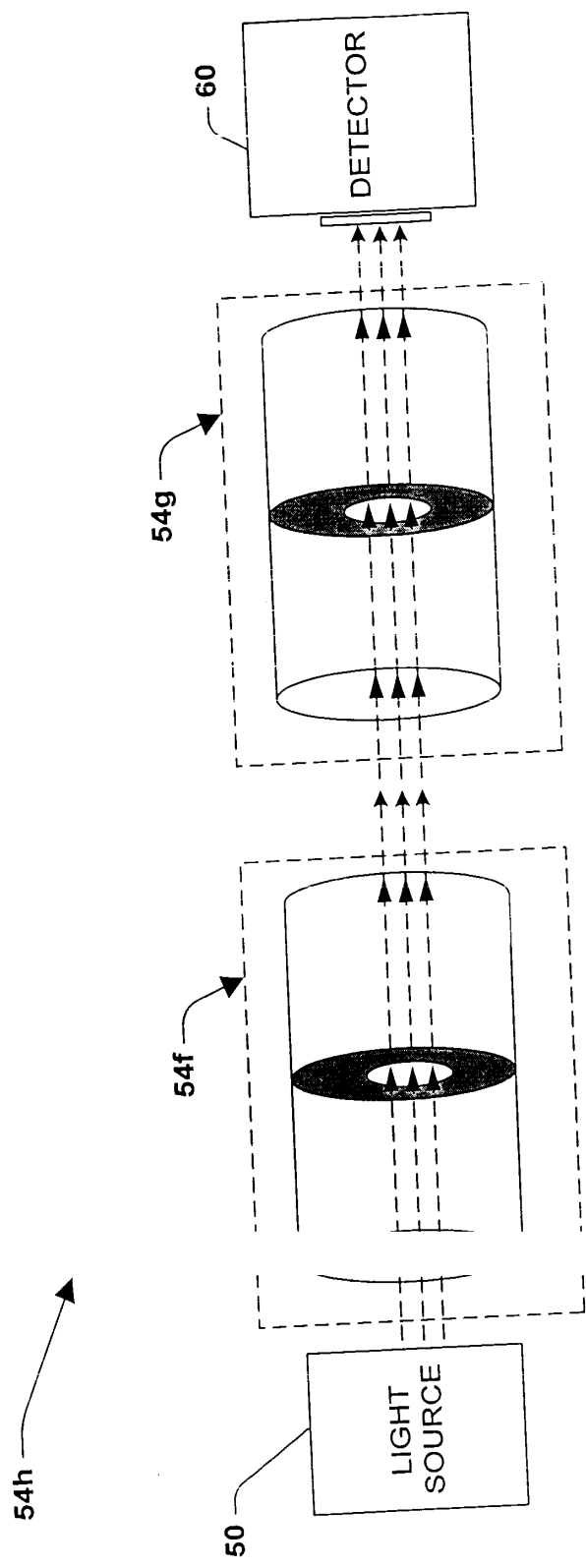


Fig. 4e

644000-1-1-0000-0000

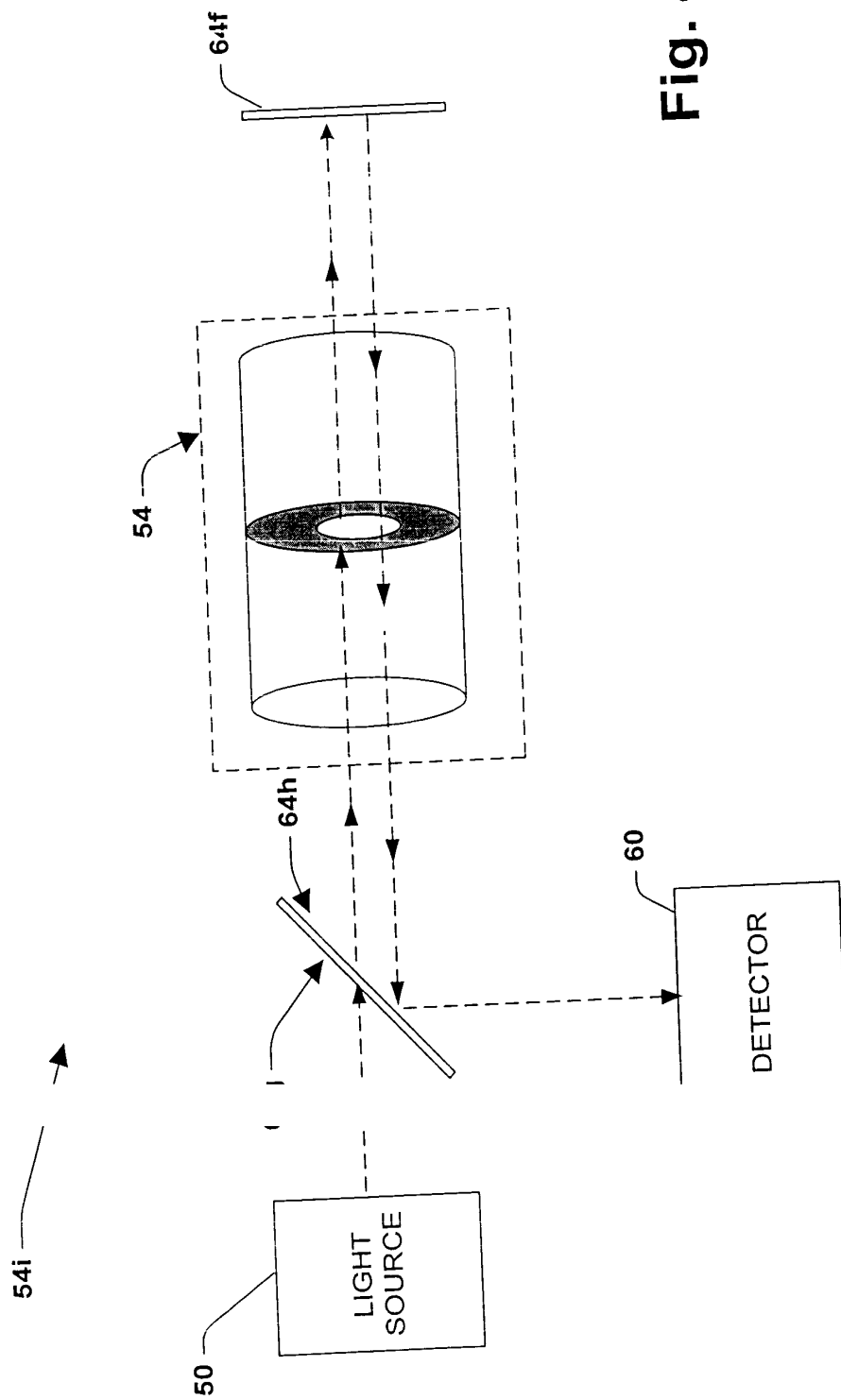


Fig. 4f

FIG. 49a 3.4.13 2007-1-10

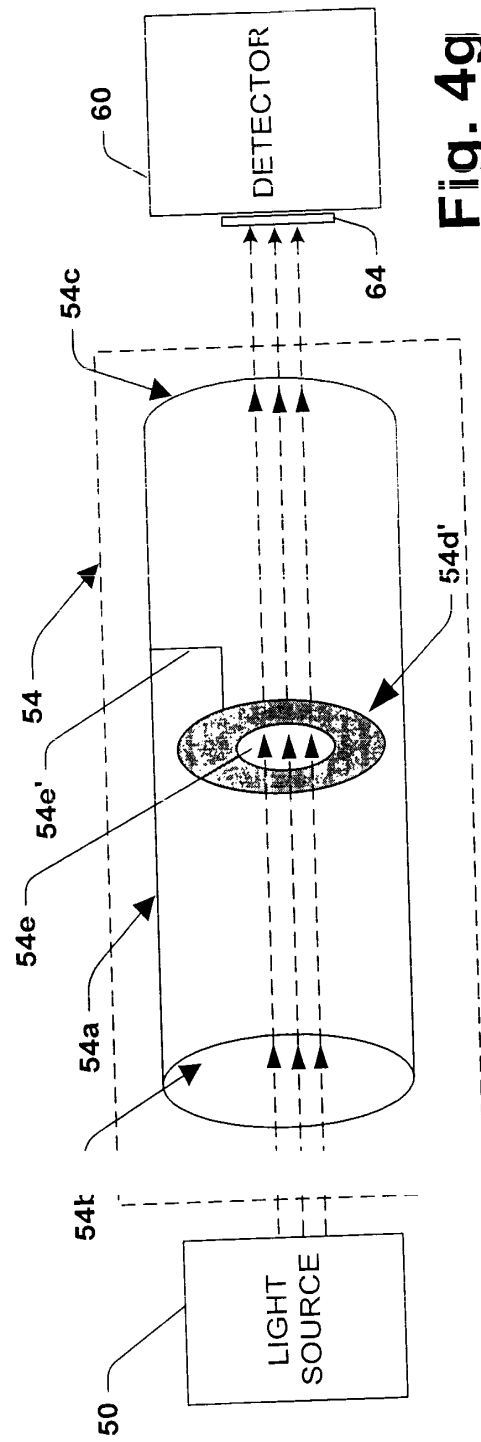


Fig. 49

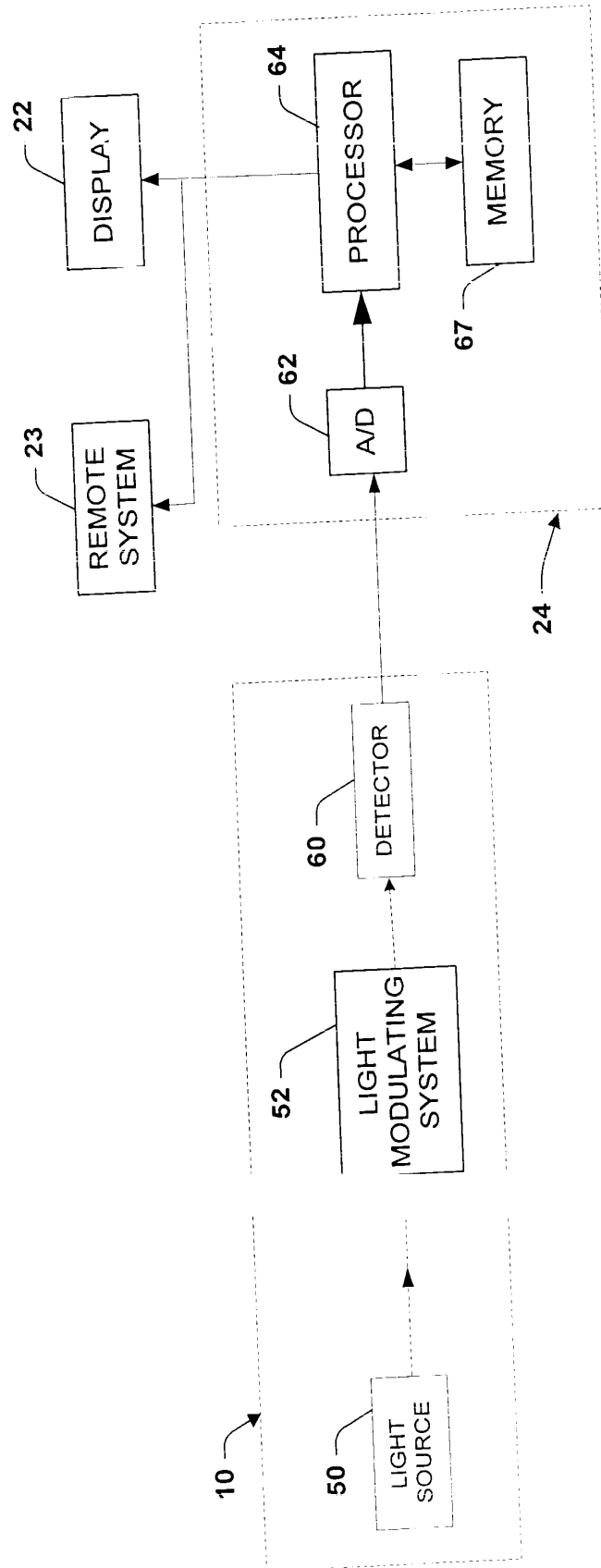


Fig. 5

Downloaded from www.scribd.com

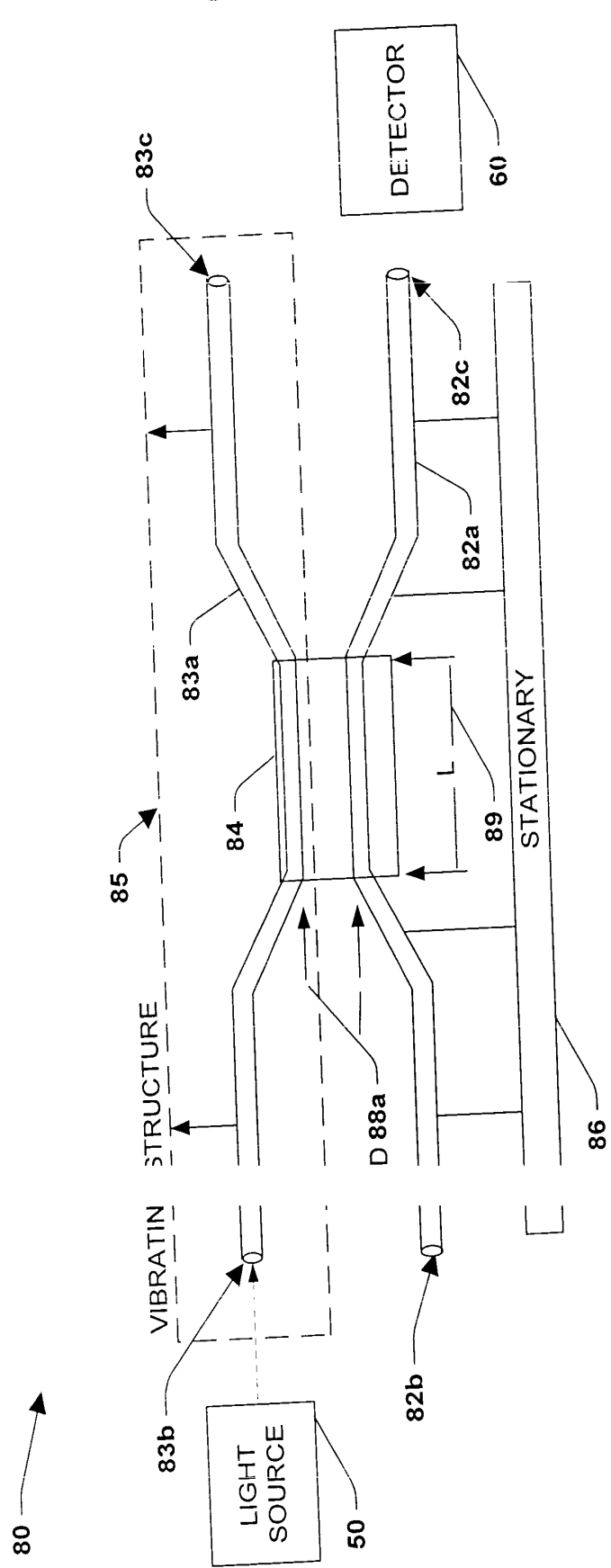


Fig. 6b

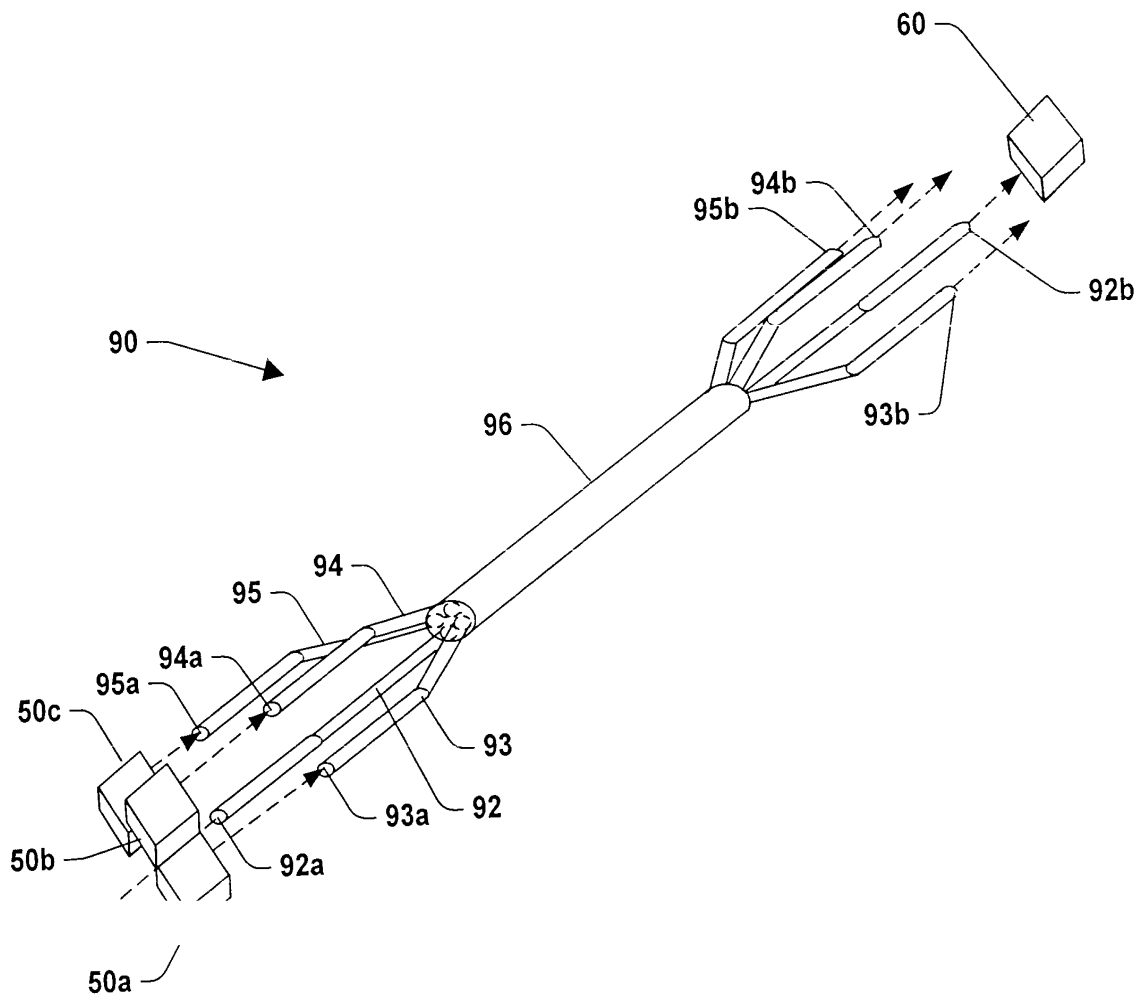


Fig. 7a

Fig. 7b

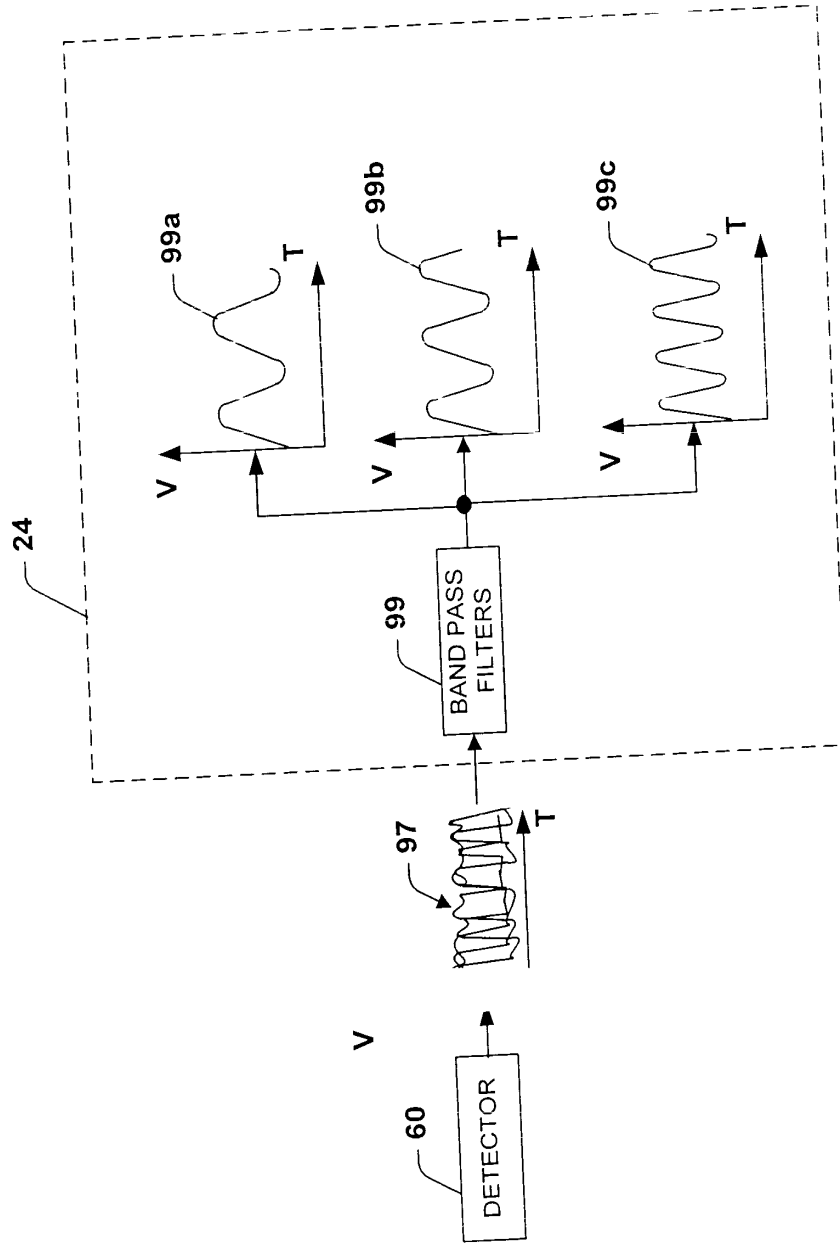


Fig. 7b

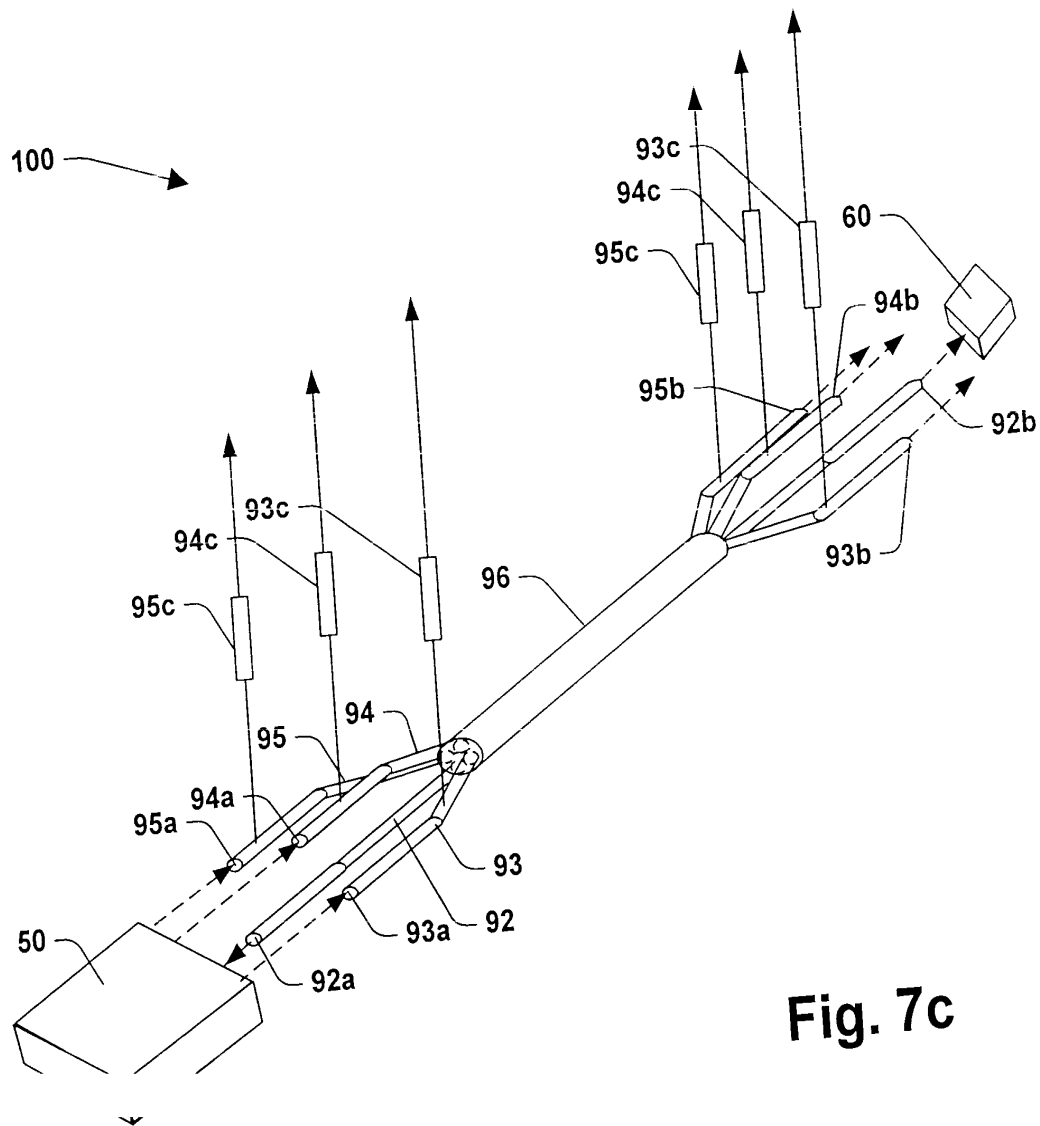


Fig. 7c

FIG. 8 is a schematic diagram of a system 110 for measuring the time delay of a signal.

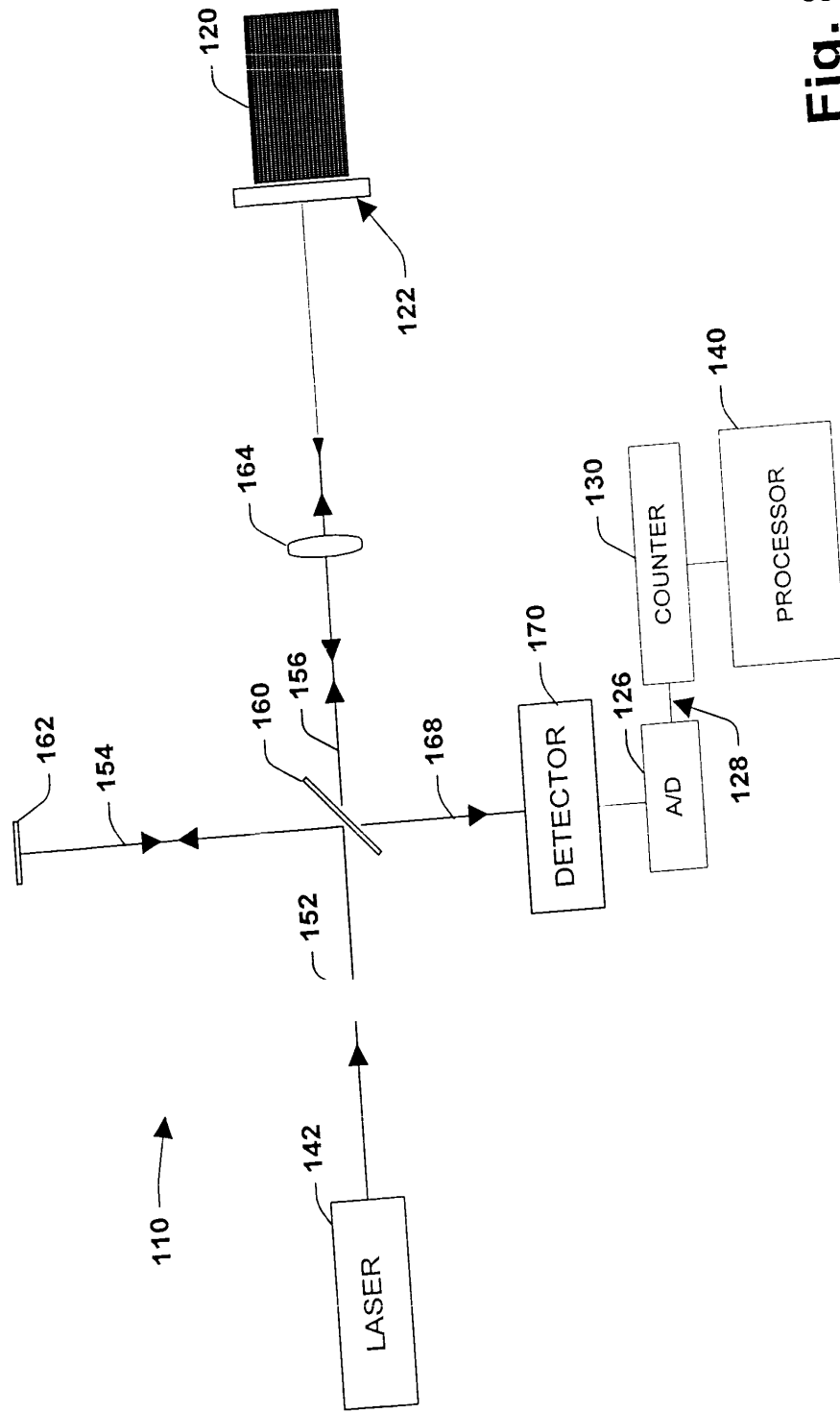


Fig. 8

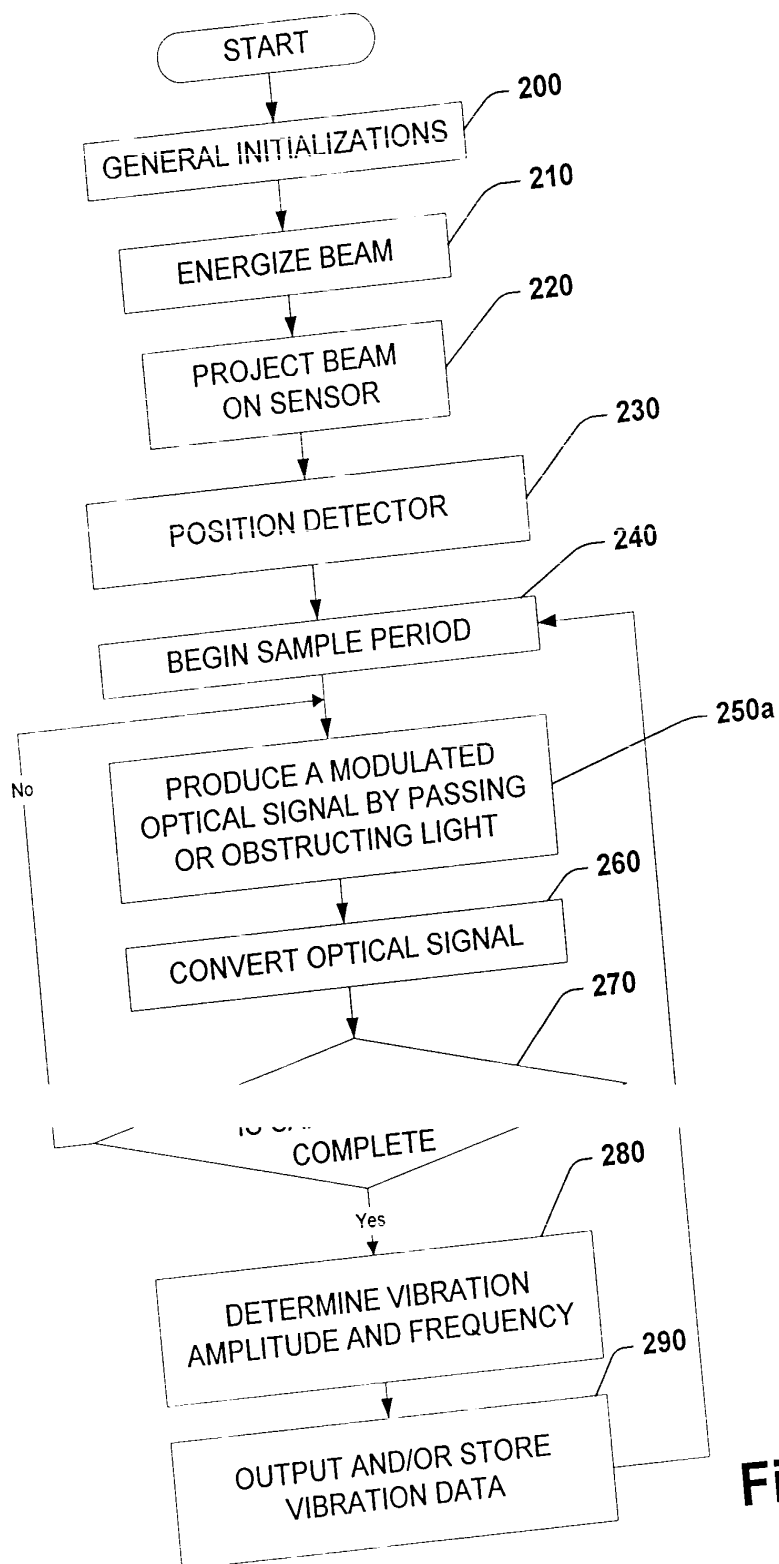


Fig. 9

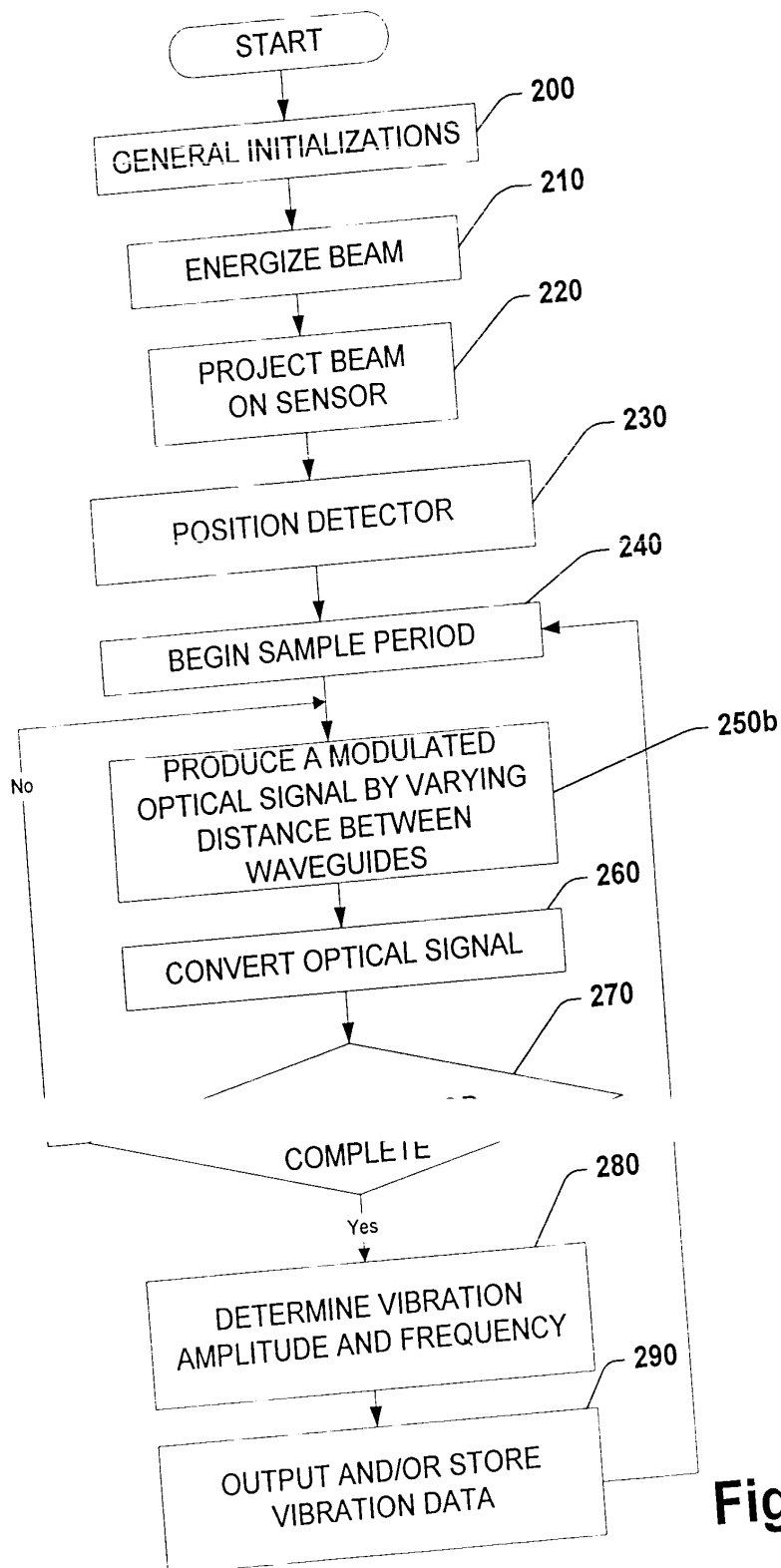


Fig. 10